

**Amendments to the Claims:**

This listing of claims replaces all prior versions and listings of claims in the patent application.

**Listing Of Claims:**

Claims 1-2. (canceled).

Claim 3. (currently amended): A data base for storing persistent data, corresponding to configuration data that is complete for configuring a terminal, comprising:

a buffer into which is written persistent data to be permanently stored;

a permanent memory connected to the buffer, the permanent memory having at least two storage areas, into which the persistent data alternately written, each storage area storing the configuration data that is complete for configuration of at least one of:

- (a) functions of the terminal,
- (b) characteristics of the terminal, and
- (c) cards of the terminal, at least one of the permanent configuration stored having a

complete configuration available and being selected for hardware implementation,

wherein the configuration data that is complete for configuration is alternately written into the storage areas by writing the complete configuration data into one of the storage areas completely and thereafter a later version of the configuration data is stored in the other storage area completely such that if the later version is lost during loading, the persistent data that is complete for configuration stored in at least one of the storage areas continues to exist and is recoverable, and wherein the persistent data stored in the buffer is alternately written into one of the storage units of the permanent memory; and

a control mechanism with a first application process for management of a first memory controls writing of the data to be persistently stored into the buffer, the data being generated or modified by the first application process alone or also by other application processes running simultaneously with the first application process, wherein only modified data sequences are alternately written into storage segments of the permanent memory.

Claims 4-5. (canceled).

Claim 6. (currently amended): The data base according to claim 53, wherein the modified data sequences are written into the storage segments of the permanent memory at predetermined time intervals.

Claim 7. (currently amended): The data base according to claim 53, wherein the modified data sequences are written into the storage segments of the permanent memory after a predetermined number of modifications.

Claim 8. (previously presented): The data base according to claim 3, wherein only the persistent data, if necessary including reconstruction data, is transferred into the buffer from a first memory which contains a run-time program and associated permanent data.

Claim 9. (previously presented): The data base according to claim 8, wherein the persistent data is stored in a space-saving manner as a data sequence in the buffer and in the permanent memory.

Claim 10. (previously presented): A data base for storing persistent data corresponding to configuration data that is complete for configuring a terminal, comprising:

a buffer into which is written persistent data to be permanently stored;

a permanent memory connected to the buffer, the permanent memory having at least first and second storage units, into which the persistent data is alternately written, each storage unit storing the configuration data that is complete for configuration of at least one of:

(a) functions of the terminal,

(b) characteristics of the terminal, and

(c) a hardware implementation, of a terminal or cards of the terminal, at least one of the permanent configurations stored having a complete configuration available and being selected for hardware implementation, wherein the configuration data that is complete for configuration is alternately written into the storage units by writing the complete configuration data into one of the storage units completely and thereafter a later version of the configuration data is stored in the other storage unit completely such that if the later version is lost during loading, the persistent data that is complete for configuration stored in at least one of the storage units continues to exist and is recoverable; and

a control mechanism within a first application process for management of a first memory controls writing of the data to be persistently stored into the buffer, the data being generated or modified by the first application process alone or also by other application processes running simultaneously with the first application process.

Claim 11. (previously presented): The data base according to claim 3, wherein if construction data which is useable for reconstruction is present in the buffer, the configuration data to be written into the first memory is automatically recovered from the reconstruction data stored in the buffer.

Claim 12. (previously presented): A data base for storing persistent data corresponding to configuration data that is complete for configuring a terminal, comprising:

a buffer into which is written persistent data to be permanently stored;

a permanent memory connected to the buffer, the permanent memory having at least first and second storage units, into which the persistent data is alternately written, each storage unit being storing the configuration data that is complete for configuration of at least one of:

(a) functions of the terminal,

(b) characteristics of the terminal, and

(c) a hardware implementation, of a terminal or cards of the terminal, at least one of the permanent configurations stored having a complete configuration available and being selected for hardware implementation, wherein the buffer has at least first and second random access memories functionally connected in series, persistent data stored in the first random access memory being written into the second random access memory so that the first random access memory is available for reloading while persistent data from the second or a further random access memory is written into the permanent memory; and

a control mechanism within a first application process for management of a first memory controls writing of the data to be persistently stored into the buffer, the data being generated or modified by the first application process alone or also by other application processes running simultaneously with the first application process.

Claim 13. (previously presented): The data base according to claim 3, wherein the permanent memory is a loadable Flash Erasable Programmable Read Only Memory chip.

Claims 14-15. (canceled).

Claim 16. (previously presented): The data base according to claim 3, wherein a number of configuration changes are only performed at a data management side and thereafter at least one of a functional and a hardware change comprising all configuration changes is performed in the terminal.

Claim 17. (previously presented): The data base according to 10, wherein all of the persistent data is stored in the buffer is alternately written into one of the storage units of the permanent memory.

Claim 18. (previously presented): The data base according to claim 17, wherein only modified data sequences are alternately written into storage segments of the permanent memory.

Claim 19. (previously presented): The data base according to claim 18, wherein the modified data sequences are written into the storage segments of the permanent memory at predetermined time intervals.

Claim 20. (previously presented): The data base according to claim 18, wherein the modified data sequences are written into the storage segments of the permanent memory after a predetermined number of modifications.

Claim 21. (previously presented): The data base according to claim 10, wherein only the persistent data, if necessary including reconstruction data, is transferred into the buffer from a first memory which contains a run-time program and associated permanent data.

Claim 22. (previously presented): The data base according to claim 21, wherein the persistent data is stored in a space-saving manner as a data sequence in the buffer and in the permanent memory.

Claim 23. (previously presented): The data base according to claim 10, wherein the permanent memory is a loadable Flash Erasable Programmable Read Only Memory chip.

Claim 24. (previously presented): The data base according to claim 10, wherein a number of configuration changes are only performed at a data management side and thereafter at least one of a functional and a hardware change comprising all configuration changes is performed in the terminal.

Claim 25. (canceled).

Claim 26. (previously presented): The data base according to 12, wherein all of the persistent data is stored in the buffer is alternately written into one of the storage units of the permanent memory.

Claim 27. (previously presented): The data base according to claim 26, wherein only modified data sequences are alternately written into storage segments of the permanent memory.

Claim 28. (previously presented): The data base according to claim 27, wherein the modified data sequences are written into the storage segments of the permanent memory at predetermined time intervals.

Claim 29. (previously presented): The data base according to claim 27, wherein the modified data sequences are written into the storage segments of the permanent memory after a predetermined number of modifications.

Claim 30. (previously presented): The data base according to 12, wherein only the persistent data, if necessary including reconstruction data, is transferred into the buffer from a first memory which contains a run-time program and associated permanent data.

Claim 31. (previously presented): The data base according to claim 30, wherein the persistent data is stored in a space-saving manner as a data sequence in the buffer and in the permanent memory.

Claim 32. (previously presented): The data base according to 12, wherein if construction data which is useable for reconstruction is present in the buffer, the configuration data to be written into the first memory is automatically recovered from the reconstruction data stored in the buffer.

Claim 33. (previously presented): The data base according to 12, wherein the permanent memory is a loadable Flash Erasable Programmable Read Only Memory chip.

Claim 34. (previously presented): The data base according to 12, wherein a number of configuration changes are only performed at a data management side and thereafter at least one of a functional and a hardware change comprising all configuration changes is performed in the terminal.